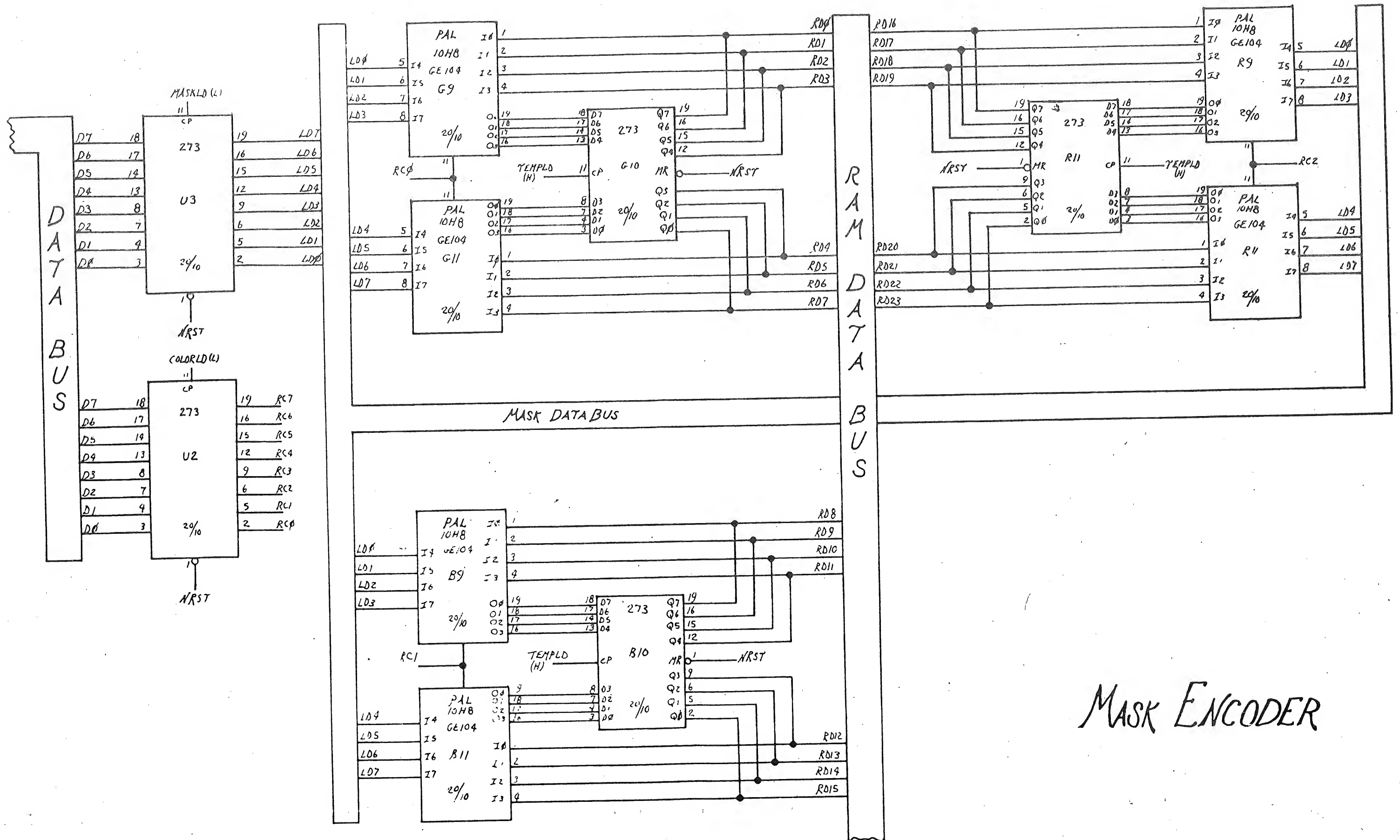
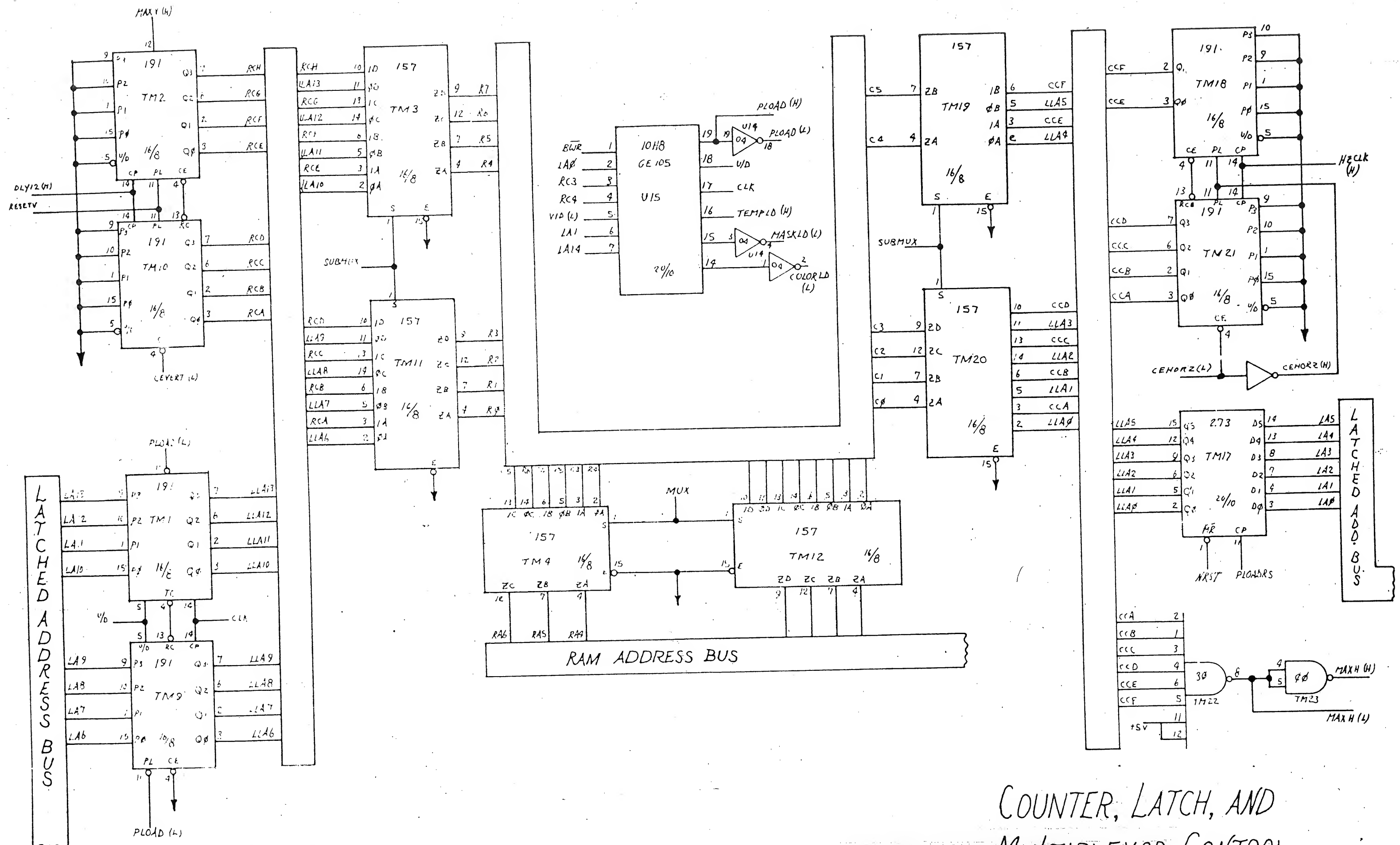


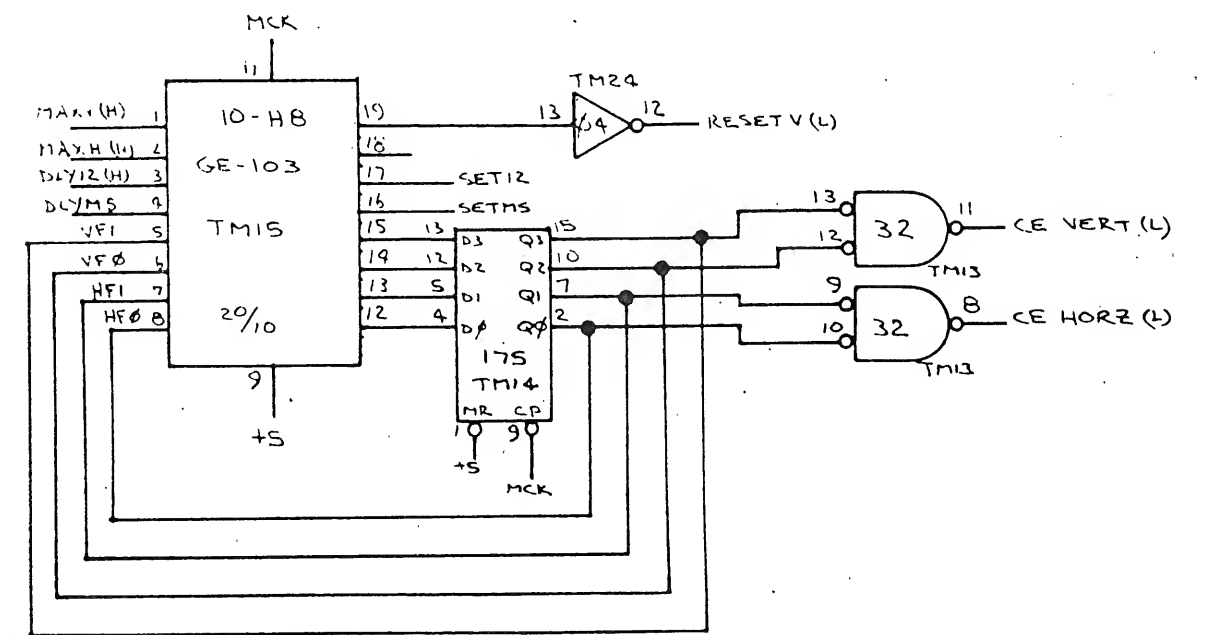
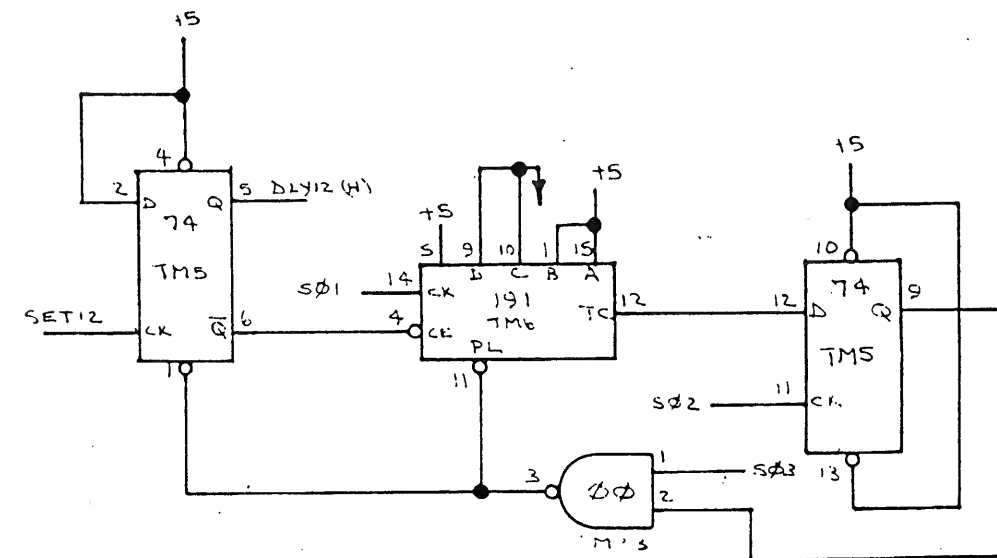
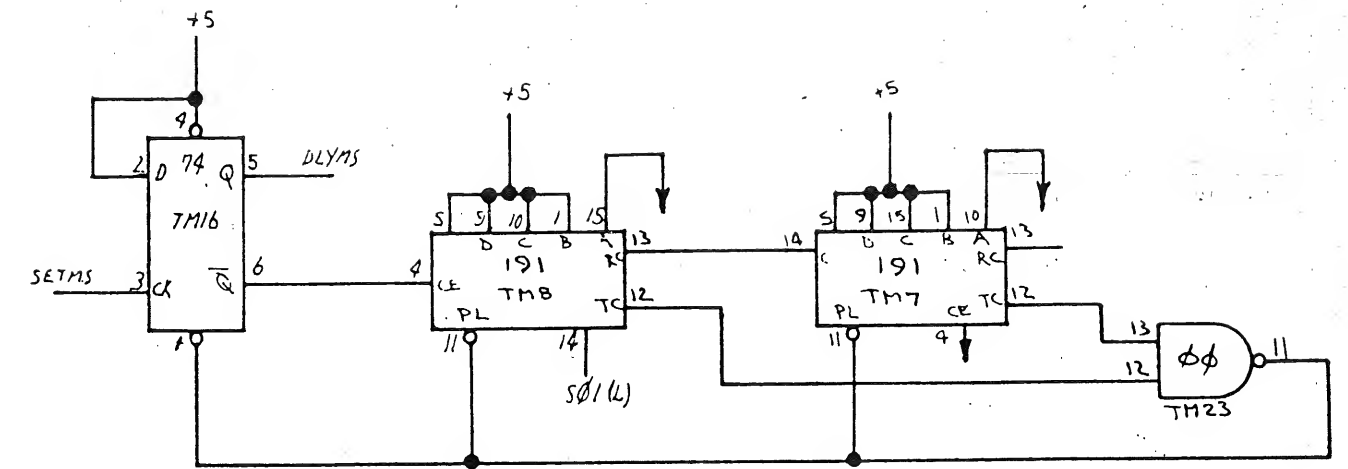
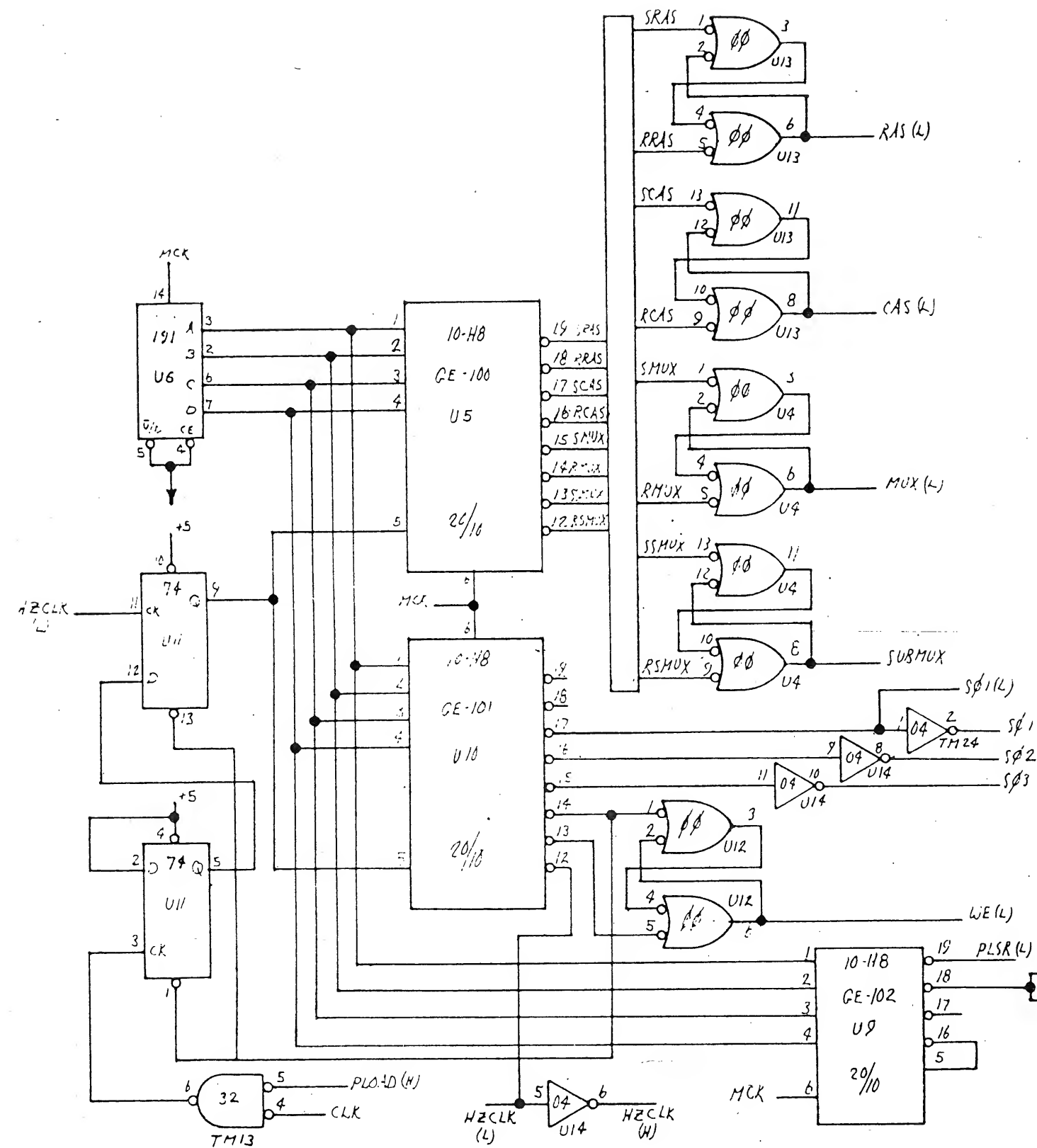
LATCHED ADDRESS BUS

CPU

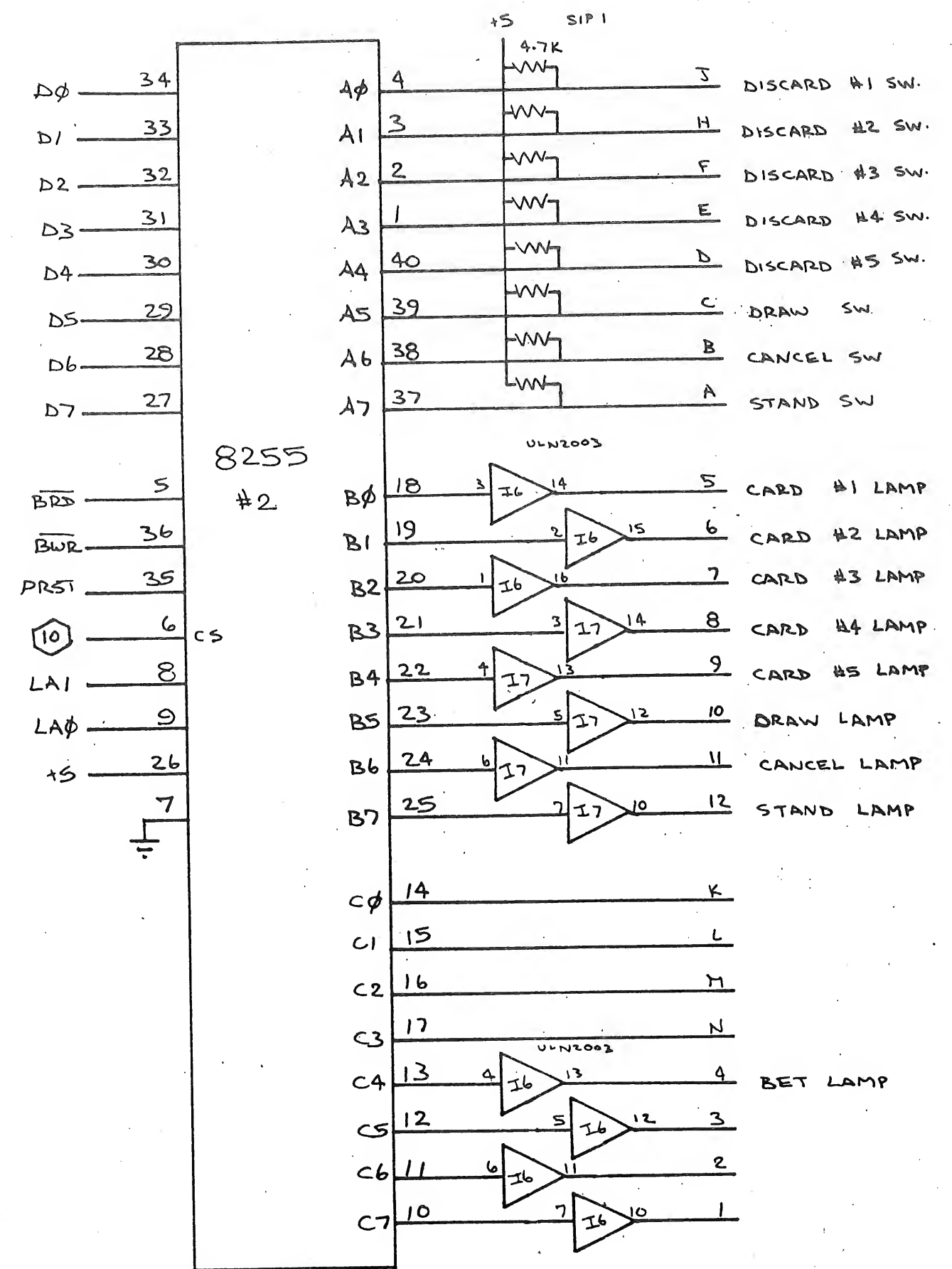
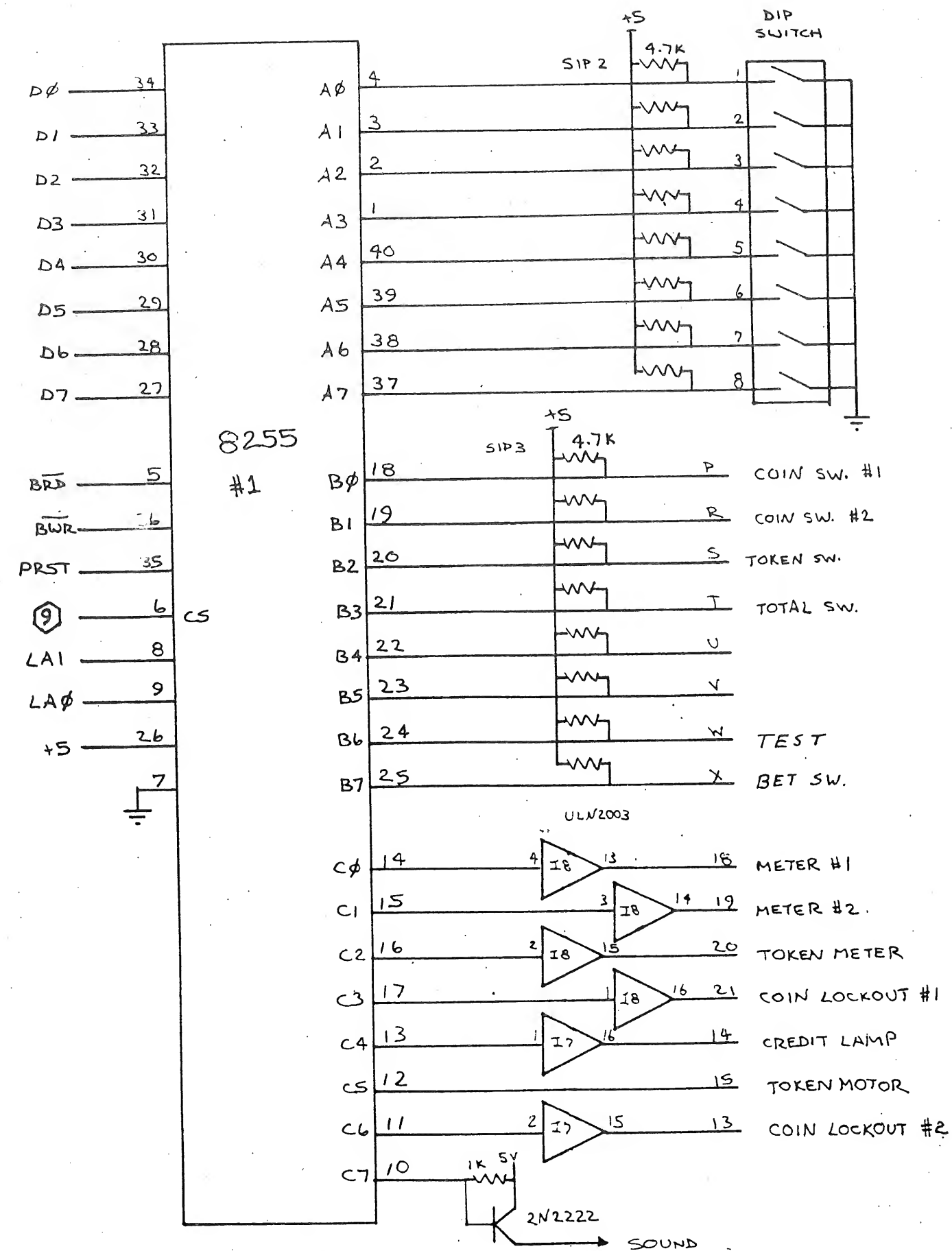




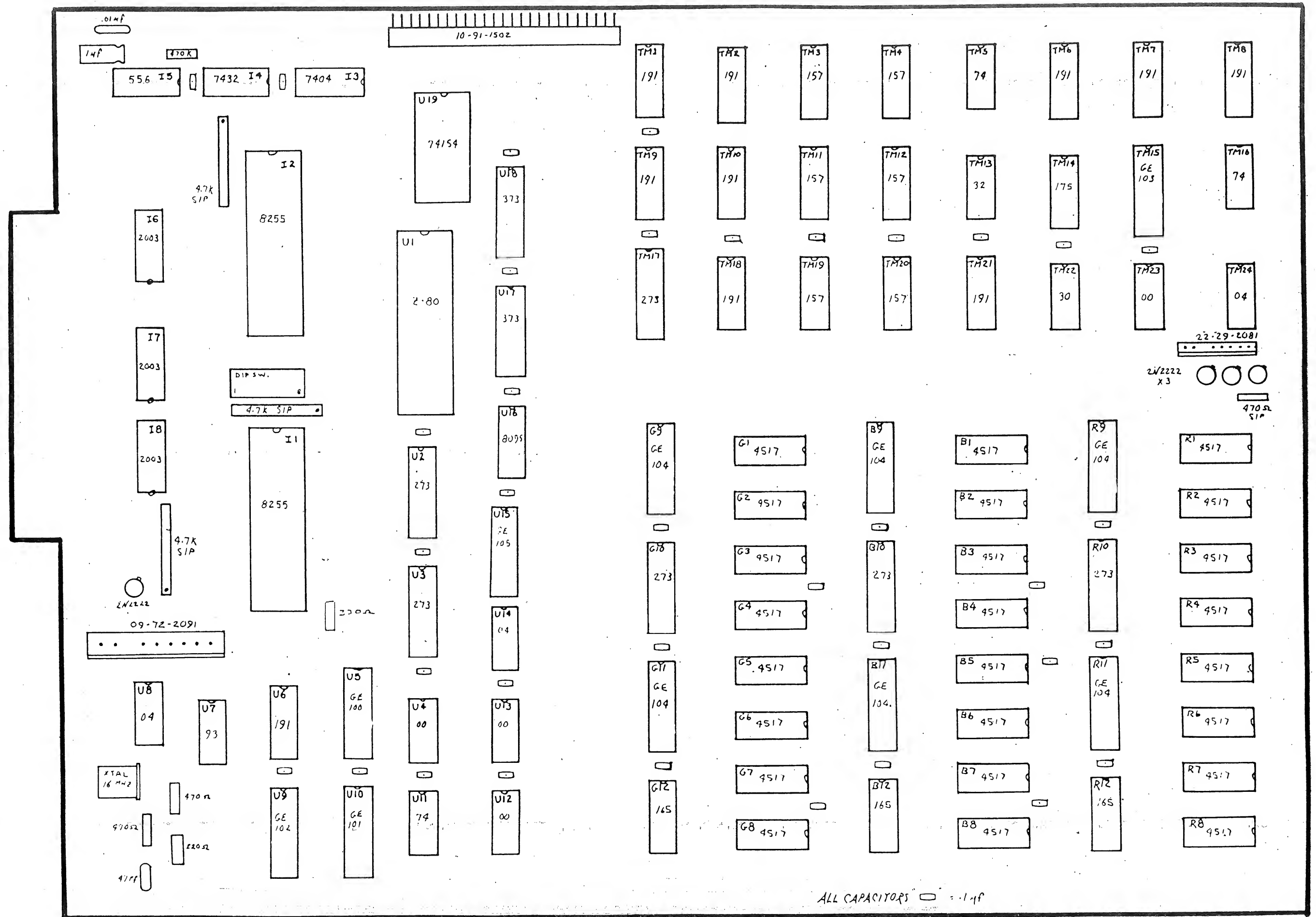
COUNTER, LATCH, AND
MULTIPLEXOR CONTROL

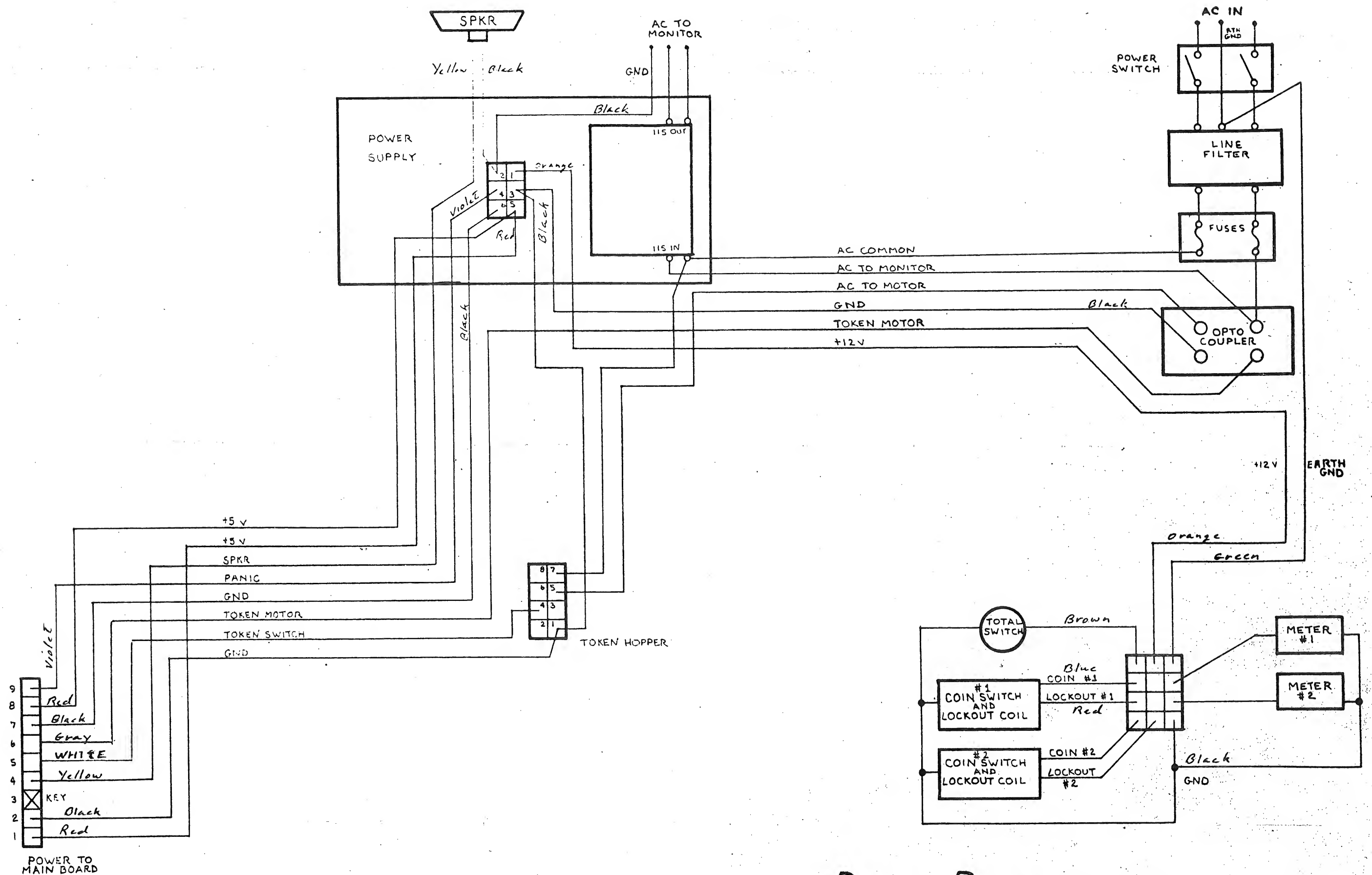


TIMING



I/O
6





POWER DISTRIBUTION

UPRIGHT ~ COUNTERTOP Fingerboard Harness

